

<b>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</b>	
In re application of: <b>Xiangdong Chen et al.</b>	Date: <b>06/29/2007</b>
Serial Number: <b>10/622,477</b>	Examiner: <b>Ngan V. Ngo</b>
Filed: <b>07/18/2003</b>	Group Art Unit: <b>2818</b>
Title: <b>Vertical MOSFET with dual work function materials</b>	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

### **AMENDMENT**

Commissioner of Patents and Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated May 31, 2007, the following Amendment is being submitted to place the above-identified application in prima facie condition for allowance.

Kindly amend the following claims and consider the following Remarks: